Week		Date		Chapter		HW due	Read Section	Laboratory		Report due
1	Tu	Jan	22	1	foundations		Ch. 1 all*	*1*	*DC measurements*	
	Th				"					
2	Tu	Jan	29		"		Appendix A	*2*	*AC measurements*	1
	Th				foundations / meet in 201 VAN	1				
3	Tu	Feb	5		diodes			*2*	*AC measurements*	
	Th			2	transistors	2***	Ch. 2 all*			
4	Tu	Feb	12		"			*3*	*diodes*	2
	Th				"	3***				
5	Tu	Feb	19	3	transistors			*3,4*	*diodes, transistors I*	
	Th				transistors + FET					
6	Tu	Feb	26	4	op amps	4***	4.01-4.20	*4,5*	*transistors I, II*	3
	Th				op amps					
7	Tu	Mar	4		op amps		4.23-4.24	*6,7*	*optoelectronics, op amps*	4,5
	Th				op amps	5				
8	Tu	Mar	11	6	opamps + comparator			*7*	*op amps*	6
	Th				*Midterm Exam*					
	Spring Vacation									
9	Tu	Mar	25		digital gates		Ch. 8 all*	*8*	*digital gates*	7
	Th				digital gates, flip/flops					
10	Tu	Apr	1		how to plan & build a project	6		*9*	*flip flops, decoder ...*	8
				5	& oscillator, 555 timer		5.12-5.19			
	Th			9	digital/analog conversion					
11	Tu	Apr	8		Lab View demonstration in room 201?			*9,10*	*flipflops/digital meets analog*	
	Th			15 & 9	optoelectronics	7	9.1 & 15.02			
12	Tu	Apr	15		student presentation			*10*	*digital meets analog*	9
	Th				student presentation					
13	Tu	Apr	22		no lecture - time for project		6.11-6.19	*project***		10
	Th				"					
14	Tu	Apr	29		"			*project***		
	Th				"			*project grading****		
15	Tu	May	6		"			*project grading****		
	We							*project grading****		
	Th				noise		7.11-end*			
							& 15.12-end			
	Th	May	15		*FINAL EXAM 9:45 AM*					
				*	skip sections marked with a box					
				**	extra lab hours scheduled by TA					
				***	requires Multisym					
				****	project is due nominally Thur May 1 at lecture time; alternatively,					
					you may (with no penalty) wait until your lab time the next week (May 6 or 7), but no later.					